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Wendell Rodrigues

PhD Student

Personal Information

Date of Birth	April 23, 1974
Place of Birth	Sobral, Ceara, Brazil
Nationality	Brazilian
Marital Status	Single

Education

2008 – now **Working for my Ph.D. in Informatics.**, *LIFL - Laboratory of Fundamental Informatics of Lille*, Université de Sciences et Technologie de Lille, France.
Home: http://www.lifl.fr/dart.

2003 **M.Sc. in Computation**, *Department of Computation*, State University of Ceara, Fortaleza-Brazil.
Home: http://www.uece.br.
GPA: 9.0 out of 10.0

Master Thesis

title *AREIA - An Approach Based on Constructivism for Computer Network Learning*

supervisors Professor Mauro Oliveira

1999 **5-year Bachelor in Electrical Engineering**, *Department of Electrical Engineering*, Federal University of Ceara, Fortaleza-Brazil.
Home: http://www.dee.ufc.br.
GPA: 8.4 out of 10.0

1994 **4-year Undergraduation in Informatics**, *Department of Informatics*, Federal Institute of Ceara, Fortaleza-Brazil.
Home: http://www.ifce.edu.br.

Undergraduate Final Work

title *Token Ring Emulation*

supervisors Professor Mauro Oliveira

Experience

Vocational

2008 – now **Student Researcher**, *University of Sciences and Technology of Lille, USTL*, Lille-France.
Teacher and Researcher of the Department of Telematics

oct,2010 – dec,2010 **PhD Intership**, *University of Hertfordshire*, Hatfield-UK, STRI Laboratory.
Exchanges in interest research areas

2003–2008 **Teacher**, *Federal Institute of Ceara*, Fortaleza-Brazil.
Teacher and Researcher of the Department of Telematics

2000–2002 **Engineering consultant**, *Matrix Internet S.A. - MATRIX*, Fortaleza-Brazil.
Supervising of computer networks and Internet provider

1995–2000 **Engineering consultant**, *D'Road Informatics and Telecomunications Ltd.*, Fortaleza-Brazil.
Supervising of computer networks and Internet provider

1994–1998 **Junior Researcher**, *DEE - Electrical Engineering Department*, Fortaleza-Brazil.
Testbed developer for HPC, MPI and PVM; Network Administrator

Miscellaneous

1993–1994 **Technician**, *Secrel S/A Consulting and Systems - SECREL*, Fortaleza-Brazil.
Setup and maintenance of mainframe terminals and printers

Languages

Portuguese	**Fluent**	*Mother Tongue*
English	**Fluent**	
French	**Fluent**	
Spanish	**Working knowledge**	

Scientific Interests

- Computer Algebra
- MDE/MDA
- Modeling of Electrical Machines
- High Performance Computing (HPC)
- Parallel Programming
- GPU, Massively Parallel Architecture
- Computer Networks

Working Experience

Operating Systems

All Microsoft™ operating systems, Solaris™ Unix, AIX Unix, MacOS™, Linux

Programming Languages

Procedural	C, Perl, PHP, Pascal
Object-Oriented	C++, Java, Object Pascal
Parallel Environments	CUDA™, OpenCL, OpenMP, MPI
Visual	Visual Basic
Assembler	Microchip PIC Assembler
Model Transformation	QVTO, Acceleo
Shells-Scripts	C, Bourne, Korn, bash, MS-DOS, VBScript, JavaScript
Web	HTML
	Tools
Administration	Apache, IIS, Sendmail and EXIM System
Data-Base	PostGres SQL™, MySQL™
Scientific	Octave, MatLab™, GNUPlot
	Office Automation
	TEX, LATEX, Microsoft Office™,OpenOffice

Supervised Students

2008 **Allan Georges Salles Ananian**, (Strictu Sensu Pos-Graduation)-Metodologia para Desenvolvimento de uma Aplicação Web para Gerenciamento de Firewall

2008 **Wlademy Damasceno Silva**, (Strictu Sensu Pos-Graduation)-Combatendo o SPAM no Ambiente Corporativo Utilizando Ferramentas Open-Source

2006 **Kátia Rejane Costa Lima**, (Strictu Sensu Pos-Graduation)-Avaliação de Segurança de Rede Corporativa usando o IDS SNORT

2005 **George Pierry**, Redes MPLS - Um Testbed usando Linux

2005 **José Belo Aragão Jr.**, Redes Peer to Peer

2004 **Marcelo Brasil**, Redes Wireless - Flexibilidade versus Vulnerabilidade

Software

Some of the following applications are on my homepage

Academic

BLAS level 1 for OpenCL
This is library which implements some matrices and vectors operations

Commercial

AVL - Automatic Vehicle Locator
This is web system for interfacing of GPS tracking environment

BarCodeWeb
Barcode web generator for using with application forms

OpenSource

HelpDesk Management System
This software allows the management of support requests

Cybercafe Management System
Cybercafe for linux based systems

Prizes

2005 First place on the Research and Postgraduate on Science and Technology, CEFET-CE V Encontro de Pesquisa e Pós-Graduação, Brazil

1995 Javacup 95 - Java Applets Awards, Sun Microsystems of Brazil

Interviews

2008 **"Programa TV Assembleia", TV Talk Show**
The theme was wireless access on the Internet

2006 **"Programa na Boca do Povo", TV Talk Show**
The theme was virus in the cellphones

2005 **"Programa Espaço de Aprendizagem", TV Talk Show**
The theme was new technologies for using with education

Invited Speaker

apr, 2010 **"GT GPU", Work Group in Paris VI**
OpenCL :: aspects of modeling and code generation

Publication Reviewer

2011 **Conexões (Journal), IFCE**

2010 **IEEE International Design and Test Workshop**

2008 **VIII ENPPG, IFCE**

Publications

W. Rodrigues, F. Guyomarc'h, J.-L. Dekeyser, and Y. Le Menach, "Automatic multi-gpu code generation applied to simulation of electrical machines," Compumag 2011, Sydney, Australia, July 2011.

J. Guo, W. Rodrigues, J. Thiyagalingam, F. Guyomarch, P. Boulet, and S.-B. Scholz, "Harnessing the power of gpus without losing abstractions in sac and arrayol: A comparative study," HIPS 2011, 16th International Workshop on High-Level Parallel Programming Models and Supportive Environments, Anchorage (Alaska), United States., May 2011.

W. Rodrigues, F. Guyomarc'h, and J.-L. Dekeyser, "A modeling approach based on uml/marte for gpu architecture," SYMPA 2011, 14th Symposium en Architectures nouvelles de machines, Saint-Malo, France, May 2011.

W. Rodrigues, F. Guyomarc'h, and D. J-L., "Using arrayol to identify potentially shareable data in thread work-groups of gpus (poster)," DEPCP 2011, Designing for Embedded Parallel Computing Platforms: Architectures, Design Tools, and Applications (in DATE Conference), Grenoble, France, February 2011.

W. Rodrigues, F. Guyomarc'h, and J.-L. Dekeyser, "Programming massively parallel architectures using marte: a case study," M-BED 2011, 2nd Workshop on Model Based Engineering for Embedded Systems Design (in DATE Conference), Grenoble, France, February 2011.

W. Rodrigues, "An MDE Approach for Automatic Code Generation from MARTE to OpenCL," tech. rep., USTL-LIFL-INRIA, 2011.

W. Rodrigues, G. F., J.-L. Dekeyser, and Y. Le Menach, "Parallel sparse matrix solver on the gpu applied to simulation of electrical machines," Compumag 2009, Florianopolis, Brazil, November 2009.

W. Rodrigues, "Análise de tráfego do protocolo rtp (real-time transport protocol) com o objetivo de contabilizar o tráfego de aplicações de voz sobre ip," VII ENPPG, Fortaleza, Brazil, November 2009.

W. Rodrigues, "(Master Thesis)AREIA – uma proposta com princípios construtivistas para o ensino de redes (an approach based on constructivism for computer network learning)," 2003.

W. Rodrigues, "Implementação em um ambiente gráfico de uma ferramenta para o ensino de redes de computadores," XVI Encontro Universitário de Inicição à Pesquisa - UFC, Brazil, 1997.

W. Rodrigues, "Papo, um ambiente integrado de simulação de redes de computadores para fins didáticos," Simpósio Brasileiro de Redes de Computadores, Brazil, 1996.

W. Rodrigues and A. E. Oliveira, "Implementação em um ambiente gráfico de uma ferramenta para o ensino de redes de computadores," XXII SEMISH - Seminário Integrado de Software e Hardware, Brazil, 1995.